# Cadence allegro 17. 2 tutorial pdf

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High-speed design High speed and high frequency design contain critical signals that need to be controlled early. With real-time analysis checks, you can identify and resolve any conflicts between signal lengths and complete your designs in a fraction of the time. Analysis Driven Design/In Design Analysis Designs are vulnerable to signal and power integrity issues due to high data transfer rates, noise, improper component setup, and even poor power delivery networks to name just a few. Poor SI and PI results in poor functionality and your design failing in the field. To avoid and facilitate these problems, it is important to simulate your design to identify and customize those signals and planes that can cause problems. Team Design & Collaboration Successfully bringing products to market requires the efforts of several team members (whether working locally or remotely). Sharing design updates manually could cost you and your team productivity and design error options. With a collaborative design environment, it's easy to avoid design data and reach the market more quickly. Re-exploring design and productivity Doesn't have to start over every project you're working on. With existing and proven design IP, you can save time and prevent design errors. By reusing known good modules and limitations, you can easily simplify the PCB design process and get to market faster with proven technology. Complex Routing Routing doesn't have to be a manual process. With different routing methods and checks, you can quickly complete complex routes on dense boards. Simply avoid breaking the rules and meet electrical, mechanical, spacing and production requirements with real-time visual cues. Miniaturization Packing complex technologies in a small area has its challenges, but with HDI technology and built-in components it is possible to maintain signal integrity and performance. Shorter signaling paths and through the elimination of stairs make it easy to create reliable, powerful and compact designs. Design for manufacturability real-time production checks such as Manufacturing Design (DFF), Assembly Design (DFA) and Test Design (DFT) can help you identify potential problems as you design so you can avoid expensive re-turns that could disrupt the project timeline. With these checks and detailed documentation, you can quickly complete your design and sign with confidence. This guide is intended for beginners in the design of printed tiles that want to complete the board using the Cadence Allegro tool. This guide is for Windows XP, but most things should be easy to extend for Linux or Unix. Those who have had experience with one or more PCB design tools can skip this page. Others may want to get a general view of the design process that follows: 1. Select the components (confectioners, sockets, etc.) to use on your board. Once you've got the list, collect the datasheets and see the suggested footprints (that is, hole sizes or pads) in those documents. In real practice follow the main categories of components used Resistors, Capacitors,Inductors, Ferrite Beads Diodes, Transistors, FETS, LEDs Connectors, Headers ICs, BGA ICs Others Interestingly, you will pay the most attention to the electrically simplest component - connectors. You must get a physical part in your hand to check the orientation and dimensions of pin number 1. 2. For each component, you need to create a footprint. An imprint is a physical representation of a component that includes holes through the plate or pads for surface bracket components. Footprints can be reused in the same board several times. In practice, you'll already have most of the footprint available and you should only create a few extra footprints for the new design. You will need to be careful to confirm that an existing print in your design library corresponds to the mechanical dimension of the component according to its datasheet. There are reference designs available from as freescale. You can have their allegro allegro and export prints that you can use after minimal or no change. 3. You need to create a schematic view of your board. This means adding different components to the board and connecting them to the wires. We'll create schematics using orcad. If you are not familiar with Orcad you may want to take orcad tutorial. 4. Once you have schematic, you need to generate a netlist and import it into the Allegro PCB Editor to complete the panel layout. Set components, define power and ground planes, redirect physical wires using this tool. Finally, you need to confirm the error board. 5. Once you have the look of the board, you generate several files called artwork or gerber. Gerber files are used by PCB manufacturers to manufacture plates. Keep hints: Most designs today use high-speed signals. You might want to get a book -- Signal Integrity for PCB designers from Amazon for $55 or for $40 here . .